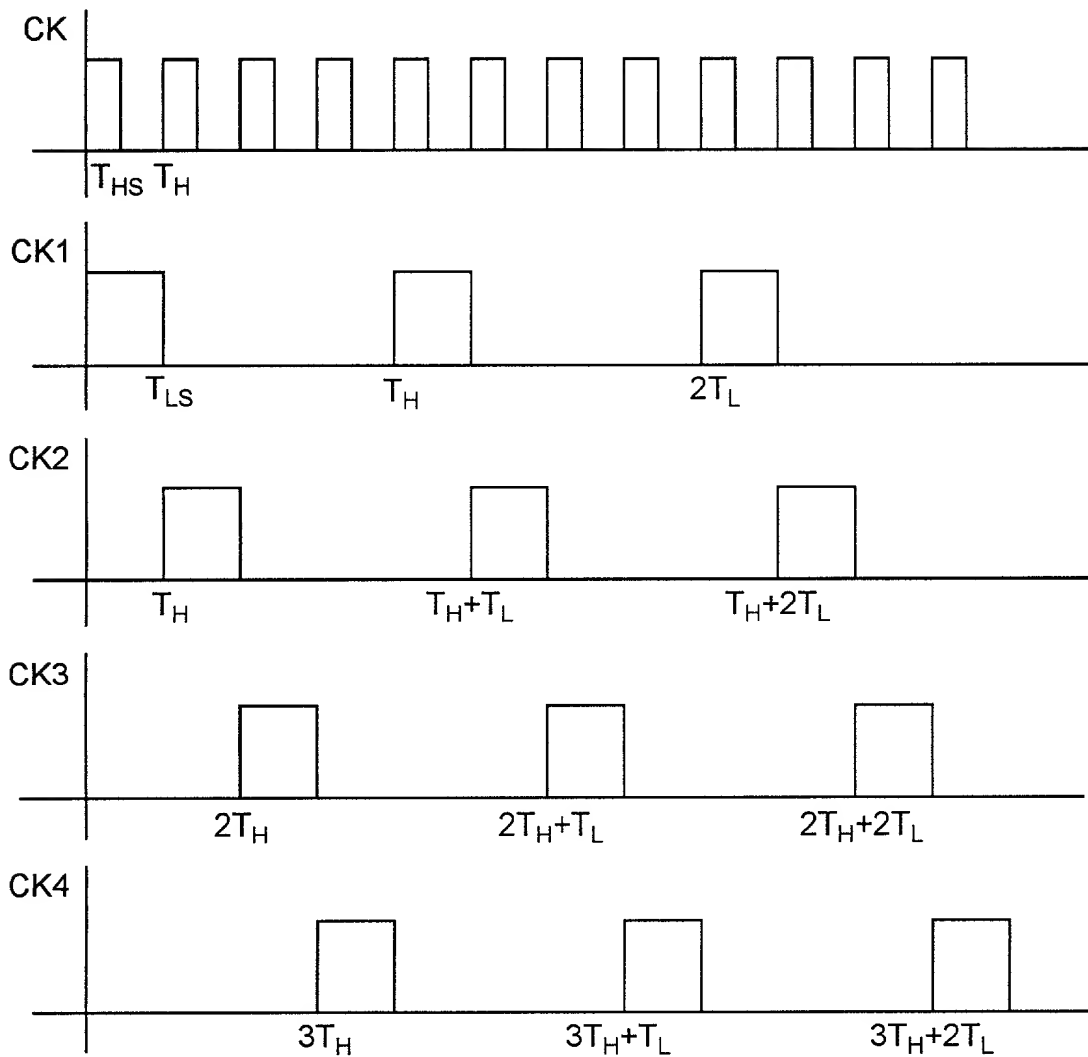


POLYPHASE CHANNELIZER

FIG. 1



POLYPHASE CLOCK TIMING DIAGRAM ($m = 4$)

FIG. 2